

1 IN THE CLAIMS

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3 Please amend claims 14, 17, and 21 as follows:

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5       1. (Original) A coupling circuit for a Serial ATA storage device, comprising:  
6           a first Serial ATA controller-side transceiver receiving a first Serial ATA  
7           communication path;  
8           a second Serial ATA controller-side transceiver receiving a second Serial ATA  
9           communication path;  
10          a Serial ATA storage device-side transceiver;  
11          coupling circuit switches selectively coupling either the first Serial ATA controller-  
12          side transceiver or the second Serial ATA controller-side transceiver to the Serial ATA  
13          storage device-side transceiver; and  
14          a microcontroller adapted to control the coupling circuit switches.

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16       2. (Original) The coupling circuit of claim 1, further comprising an out of band  
17          squench control component for activating the first Serial ATA controller-side transceiver  
18          receiving a first Serial ATA communication path, the second Serial ATA controller-side  
19          transceiver receiving a second Serial ATA communication path, and the Serial ATA  
20          storage device-side transceiver.

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22       3. (Original) The coupling circuit of claim 1, wherein the microcontroller  
23          includes a processor coupled to a power switch and coupled to the coupling circuit  
24          switches.

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26       4. (Original) The coupling circuit of claim 1, wherein the microcontroller  
27          includes a processor coupled to a set of D flip-flops that are coupled to a power switch  
28          and coupled to the coupling circuit switches.

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1       5. (Original) The coupling circuit of claim 1, wherein the microcontroller is  
2 programmed to as follows:

3           switch the coupling circuit to a first storage controller;  
4           switch the coupling circuit to a second storage controller;  
5           power up the Serial ATA storage device; and  
6           power down the Serial ATA storage device.

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8       6. (Original) The coupling circuit of claim 5, wherein the microcontroller is  
9 further programmed to as follows:

10           write data to a memory;  
11           read data from the memory; and  
12           read the status of the coupling circuit.

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14       7. (Original) The coupling circuit of claim 6, wherein the status includes  
15 information on whether the Serial ATA storage device is coupled to the first Serial ATA  
16 controller-side transceiver or the second Serial ATA controller-side transceiver, the  
17 Serial ATA storage device is powered up or down, the communication status, and/or the  
18 board revision and code revision levels of the coupling circuit.

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20       8. (Original) A method of controlling Serial ATA storage devices in a data  
21 storage subsystem, comprising:

22           connecting the Serial ATA storage devices to a first storage controller;  
23           reading the identity of each of the Serial ATA storage devices;  
24           dividing the Serial ATA storage devices into set(s);  
25           assigning each set to the first storage controller or a second storage controller;  
26 and  
27           coupling the Serial ATA storage devices as assigned to the first storage  
28 controller or the second storage controller.

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30       9. (Original) The method of claim 8, wherein the step of assigning includes  
receiving a host I/O command in the first storage controller.

1           10. (Original) The method of claim 8, wherein the step of coupling includes the  
2 first controller instructing the second storage controller to couple to the set(s) of Serial  
3 ATA storage devices.

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5           11. (Original) The method of claim 8, wherein the step of coupling includes  
6 coupling the set(s) of Serial ATA storage devices to the first storage controller and  
7 notifying the second storage controller.

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9           12. (Original) The method of claim 8, wherein the step of dividing the Serial  
10 ATA storage devices results in two sets of Serial ATA storage devices plus a spare.

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12           13. (Original) The method of claim 8, wherein the step of coupling includes  
13 coupling all of the Serial ATA storage devices to the first storage controller and coupling  
14 all of the Serial ATA storage devices to the second storage controller if the first  
15 controller fails.

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17           14. (Amended) A data storage system for assigning control of Serial ATA  
18 storage devices, wherein each Serial ATA storage device connects through coupling  
19 circuit switches to storage controllers, comprising:  
20           a host sending an I/O command identifying Serial ATA storage devices;

21           a first storage controller receiving the I/O command and commanding a  
22 microcontroller coupled to the coupling circuit switches to connect the Serial ATA  
23 storage devices identified in the I/O command to the first storage controller.

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25           15. (Original) The data storage system of claim 14, wherein the first storage  
26 controller is programmed to read the identity of each of the Serial ATA storage devices  
27 and divide the Serial ATA storage devices into set(s).

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29           16. (Original) The data storage system of claim 14, further comprising a  
30 second storage controller programmed to read the identity of each of the Serial ATA  
storage devices and divide the Serial ATA storage devices into set(s).

1        17. (Amended) A data storage subsystem for controlling Serial ATA storage  
2 devices, wherein each Serial ATA storage device connects through a coupling circuit  
3 containing a microcontroller switches to storage controllers, comprising:

4              a first storage controller; and  
5              a second storage controller, wherein the first storage controller assigns the Serial  
6 ATA storage devices to the first storage controller or the second storage controller and  
7 commands the microcontroller coupling circuit switches to correspondingly connect the  
8 Serial ATA storage devices to the first storage controller or the second storage  
9 controller.

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11        18. (Original) The subsystem of claim 17, wherein the first storage controller  
12 reads the identity of each of the Serial ATA storage devices and divides the Serial ATA  
13 storage devices into set(s).

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15        19. (Original) The subsystem of claim 18, wherein the first storage controller  
16 assigns the set(s) to the second storage controller and instructs the second storage  
17 controller to switch to the set(s) of Serial ATA storage devices.

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19        20. (Original) The subsystem of claim 18, wherein the first storage controller  
20 assigns the set(s) to the second storage controller, switches the set(s) to the second  
21 storage controller, and notifies the second storage controller of the assignment.

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23        21. (Amended) A method of restoring operation of a Serial ATA storage  
device, comprising:

24              detecting the Serial ATA storage device has failed to respond to an I/O command  
25 within a predetermined time;

26              commanding a coupling circuit containing a microcontroller to power down the  
27 Serial ATA storage device for a predetermined time; and

28              commanding a coupling circuit containing a microcontroller to power up the Serial  
29 ATA storage device.  
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1           22. (Original) A coupling circuit for a storage device, comprising:  
2           a first controller-side transceiver receiving a first communication path;  
3           a second controller-side transceiver receiving a second communication path;  
4           a storage device-side transceiver;  
5           coupling circuit switches selectively coupling either the first controller-side  
6           transceiver or the second controller-side transceiver to the storage device-side  
7           transceiver; and  
8           a microcontroller adapted to control the coupling circuit switches and control the  
9           power to the storage device.

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11         23. (Original) A coupling circuit for a Serial ATA storage device, comprising:  
12           means for receiving a first Serial ATA communication path;  
13           means for receiving a second Serial ATA communication path;  
14           means for coupling either the first Serial ATA communication path or the second  
15           Serial ATA communication path to the Serial ATA storage device; and  
16           a microcontroller adapted to control the coupling circuit switches.

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